



FIG. 1

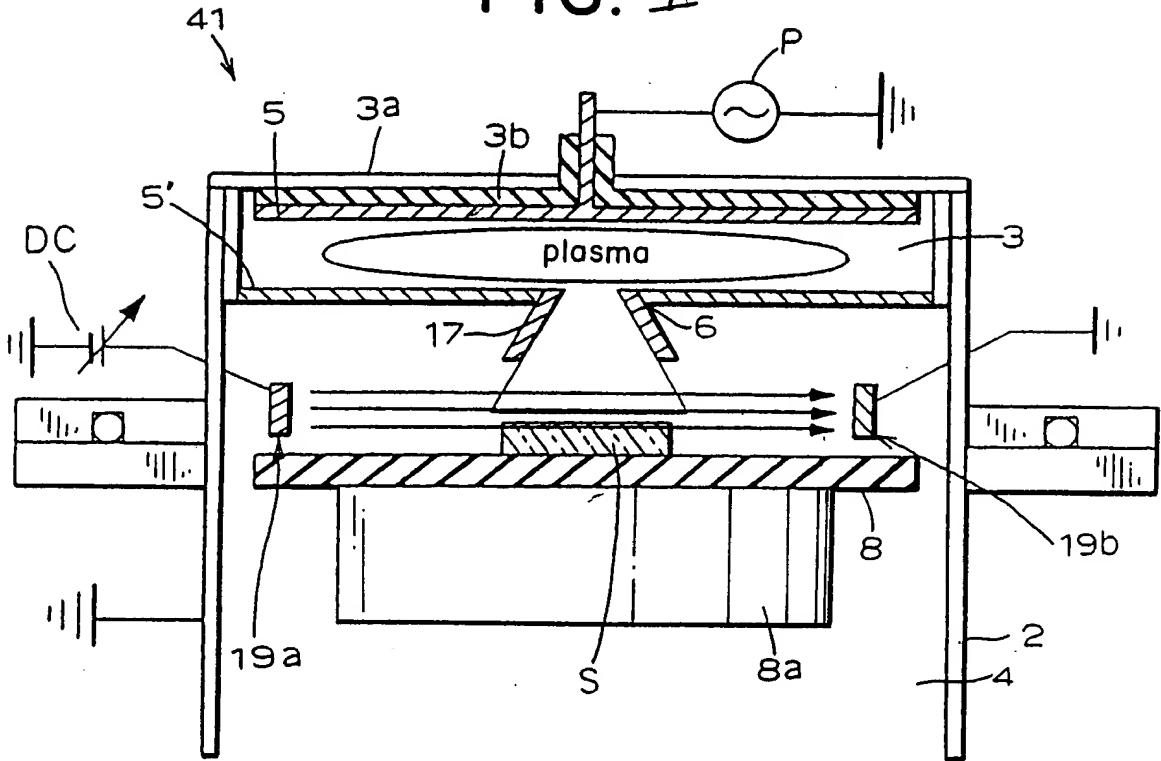
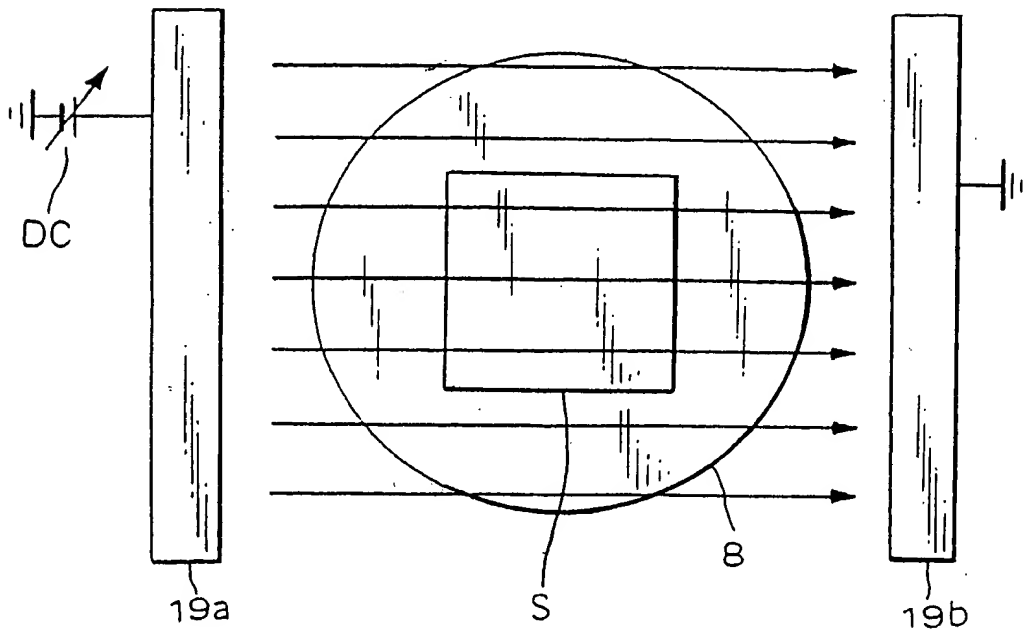


FIG. 2



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A schematic diagram of a photoconductive device. It features a substrate 4 with a photoconductive layer 8. A gate electrode 39 is positioned above the photoconductive layer 8, with a gate insulating layer 10 between them. A source electrode 26 is also on the photoconductive layer 8. A drain electrode 5' is connected to the gate electrode 39. A feedback circuit is connected between the drain electrode 5' and the gate electrode 39, consisting of a variable resistor 5 and a power source P. A DC voltage source is connected to the source electrode 26 and the gate electrode 39 through a variable resistor.

[illegible]

[illegible]

The diagram illustrates a magnetic field measuring device. It features a U-shaped magnet assembly with a central gap. The upper part of the magnet is labeled 5, and the lower part is 5'. A central core is labeled 20. The gap is defined by parts 49 and 46. A coil, labeled 8, is positioned within the gap. A DC current source is connected to the coil, and an AC voltmeter, labeled P, is connected to the magnet assembly. A switch, labeled S, is also shown.

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The diagram illustrates a magnetic head assembly. It features a cross-section of a head with several layers and components. The top layer is labeled 5, and the layer immediately below it is 5'. A gap layer, labeled 6', is positioned between the head and the pole piece 30a. The pole piece 30a is connected to a coil 39, which is in turn connected to a pole piece 30d. The coil 39 is connected to an AC source P. The pole piece 30d is connected to a DC source with a variable resistor. The pole piece 30d is also connected to a sliding contact S, which is in contact with a base layer 56. The base layer 56 is mounted on a substrate 8. The gap layer 6'' is located between the head and the pole piece 30d. The layer 30e is located between the head and the pole piece 30d.

FIG. 1 is a perspective view of a first embodiment of a device. The device includes a substrate 39. On the top surface of the substrate 39, there are a series of parallel strips 10. Each strip 10 is composed of a top layer 5' and a bottom layer 76. A DC voltage source is connected to the bottom layer 76 of the strips.

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